



DOCKET NO.: 93-C-091D1
PATENT NO.: STMI01-00012
Customer No. 30425

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: : PERVEZ H. SAGARWALA ET AL
Serial No. : 09/800,039
Filed : March 6, 2001
For : CMOS INTEGRATED CIRCUIT DEVICE WITH LDD N-
CHANNEL TRANSISTOR AND NON-LDD P-CHANNEL
TRANSISTOR
Group No. : 2822
Examiner : M.V. Prenty

BOX AF
Commissioner for Patents
Washington, D.C. 20231

Sir:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

The undersigned hereby certifies that the following documents:

1. Postcard receipt;
2. Check in the amount of \$320.00 for the Notice of Appeal filing fee;
3. Notice of Appeal;
4. Amendment and Response to Final Office Action.

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to: BOX AF, Commissioner for Patents, Washington, D.C. 20231, on February 10, 2003.

Date: 2-10-03

Date: 2-10-03

Mailer

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Customer No. 30425

#8/Response
PATENT
2/3/03

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Examiner : M.V. Prenty

BOX NON AF
Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

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RESPONSE TO FINAL OFFICE ACTION

No fees are believed to be necessary; however, in the event that any additional fees are required for the prosecution of this application, please charge any necessary fees to Deposit Account No. 50-0208. No extension of time is believed to be necessary. If, however, an extension of time is needed, the extension is requested and please charge the fee for this extension to Deposit Account No. 50-0208.

In response to the final Office Action dated November 8, 2002, please amend the above-identified application as follows:

OK
to
enter
MP